

**UNIVERSITY COLLEGE OF ENGINEERING (Autonomous)
OSMANIA UNIVERSITY**

**M. E. (Civil) IV - Semester (PTPG) Make-up Examination
October 2016**

**Examination Centre:
Main Building
UCE (A), O.U.**

EXAMINATION TIME TABLE

Time : 2.00 PM to 5.00 PM

| Date and Day | Structural Engineering | Transportation Engg. |
|------------------------|-------------------------------|---------------------------------------------------|
| | PTPG IV - Semester | PTPG IV - Semester |
| 25-10-2016 TUESDAY | Bridge Engineering | Railway Engineering |
| 27-10-2016 THURSDAY | Advanced Concrete Technology | Airport Planning and Design |
| 29-10-2016 SATURDAY | Advanced Steel Design | Pavement Evaluation Maintenance and Management |


13.10.2016

**DIRECTOR OF EVALUATION
EXAMINATION CELL**

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UCE (A), O.U.**

UNIVERSITY COLLEGE OF ENGINEERING

OSMANIA UNIVERSITY

M.E. (Elect. Engg.) IV - Semester (PTPG) Make-up Examination

October 2016

EXAMINATION TIME TABLE

Time : 2.00 PM to 5.00 PM

| Date & Day | Industrial Drives & Control |
|------------------------|----------------------------------------|
| | PTPG IV – Semester |
| 25-10-2016 TUESDAY | High Voltage Engineering |
| 27-10-2016 THURSDAY | Control of Electric Drives |
| 29-10-2016 SATURDAY | Modern Control Theory |


13.10.2016

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UNIVERSITY COLLEGE OF ENGINEERING (Autonomous)

OSMANIA UNIVERSITY, HYDERABAD

M.E. (E.C.E) IV - Semester (PTPG) Make-up Examination

October 2016

EXAMINATION TIME TABLE

Time : 2.00 PM to 5.00 PM

Examination Centre:
Main Building
UCE (A), O.U.

| Date and Day | Digital System Engineering | Microwave & Radar Engineering | Systems & Signal Processing |
|------------------------|---------------------------------------------|-------------------------------------|---------------------------------------------|
| | PTPG IV-Semester | PTPG IV-Semester | PTPG IV-Semester |
| 25-10-2016 TUESDAY | Advanced Digital Design with Verilog HDL | Phased Array Radar | Advanced Digital Design with Verilog HDL |
| 27-10-2016 THURSDAY | System on Chip Architecture | Microwave Solid State Devices | Software Defined Radio |
| 29-10-2016 SATURDAY | Low Power VLSI Design | Optical Fiber Communication Systems | Image and Video Processing |


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UNIVERSITY COLLEGE OF ENGINEERING (Autonomous)

OSMANIA UNIVERSITY

M. E. (Mechanical) IV - Semester (PTPG) Make-up Examination

October 2016

EXAMINATION TIME-TABLE

TIME : 2.00 PM TO 5.00 PM

**Examination Centre:
Main Building
UCE (A), O.U.**

| Date & Day | Production Engineering | Automation & Robotics | Turbo Machinery |
|--------------------------------|---------------------------------------------------|-------------------------------------------------------|------------------------------|
| | PTPG IV-Semester | PTPG IV-Semester | PTPG IV-Semester |
| 25-10-2016 TUESDAY | Advanced Non-Destructive Evaluation Techniques | Additive Manufacturing Technologies & Applications | Computational Fluid Dynamics |
| 27-10-2016 THURSDAY | Product Design and Process Planning | Computer Integrated Manufacturing | Fuels and Combustion |
| 29-10-2016 SATURDAY | Flexible Manufacturing Systems | Fluid Power Systems | Design of Gas Turbines |


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**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD**

**M. Tech. (C.S.E.) IV - Semester (PTPG) Make-up Examination
October 2016**

**Examination Centre:
Main Building
UCE (A), O.U.**

EXAMINATION TIME TABLE

Time: 2.00 PM to 5.00 PM

| DATE & DAY | CSE |
|------------------------|-------------------------------|
| | PTPG IV - SEMESTER |
| 25-10-2016 TUESDAY | Distributed Computing |
| 27-10-2016 THURSDAY | Image Processing |
| 29-10-2016 SATURDAY | Software Reuse Techniques |


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